



深圳市一众显示科技有限公司

SHEN ZHEN TEAM SOURCE DISPLAY TECH. CO, LTD.

TFT-LCD Module Specification

Module NO.: TST070WSSN-42

Version: V1.0

☐ APPROVAL FOR SPECIFICATION

☐ APPROVAL FOR SAMPLE

For Customer' s Acceptance:	
Approved by	Comment

Team Source Display:		
Presented by	Reviewed by	Organized by

Version No.	Date	Content	Remark
V1.0	2019-07-10	Initial Release	

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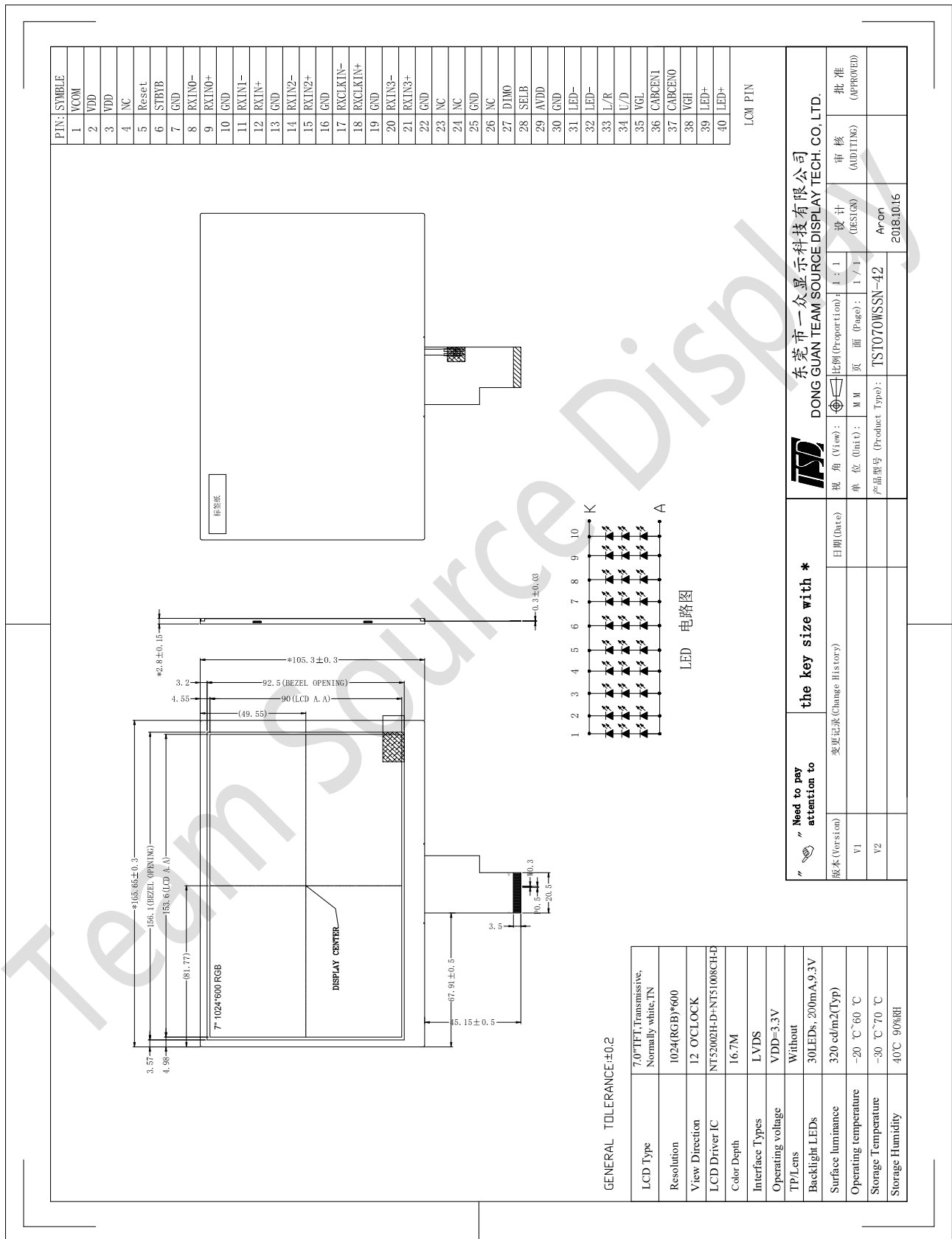
1 General Characteristics

ITEM	Specification	Unit
LCD Type	a-Si TFT, Transmissive, Normallywhite, TN	-
LCD Size	7.0	inch
Resolution (W x H)	1024x (RGB) × 600	pixel
LCM size	165.65(H) x 105.3(V) x 2.8(T)	mm
Active Area	153.6 (H) x 90.0 (V)	mm
Dot Pitch	0.05(H) × 0.15(V)	mm
Viewing Direction	6 o'clock	-
Color Depth	16.7M	-
Pixel Arrangement	RGB-stripe	-
Backlight Type	Side-Light (30leds) , 200mA, 9.3V(REF.)	-
Surface Luminance	320 TYP	cd/m ²
Surface Treatment	-	-
LCD Driver IC	NT52002H-D+NT51008CH-D	-
Interface Type	LVDS	-
Input Voltage	3.3	V
With/Without TP	Without	-
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.

2 Product drawings



3 Interface description

PIN NO.	Symbol	description
1	VCOM	Common voltage
2-3	VDD	Power voltage for digital circuit
4	NC	No connect
5	RESET	Global reset pin
6	STBYB	Standby mode control. Normally pull High. When STBYB=H,normal operation When STBYB=L, timing controller,source driver will turn off,all output are High-Z.
7	GND	Power ground
8	RXIN0-	Negative LVDS differential data input
9	RXIN0+	Positive LVDS differential data input
10	GND	Power ground
11	RXIN1-	Negative LVDS differential data input
12	RXIN1+	Positive LVDS differential data input
13	GND	Power ground
14	RXIN2-	Negative LVDS differential data input
15	RXIN2+	Positive LVDS differential data input
16	GND	Power ground
17	RXCLKIN-	Negative LVDS differential clock input
18	RXCLKIN+	Positive LVDS differential clock input
19	GND	Power ground

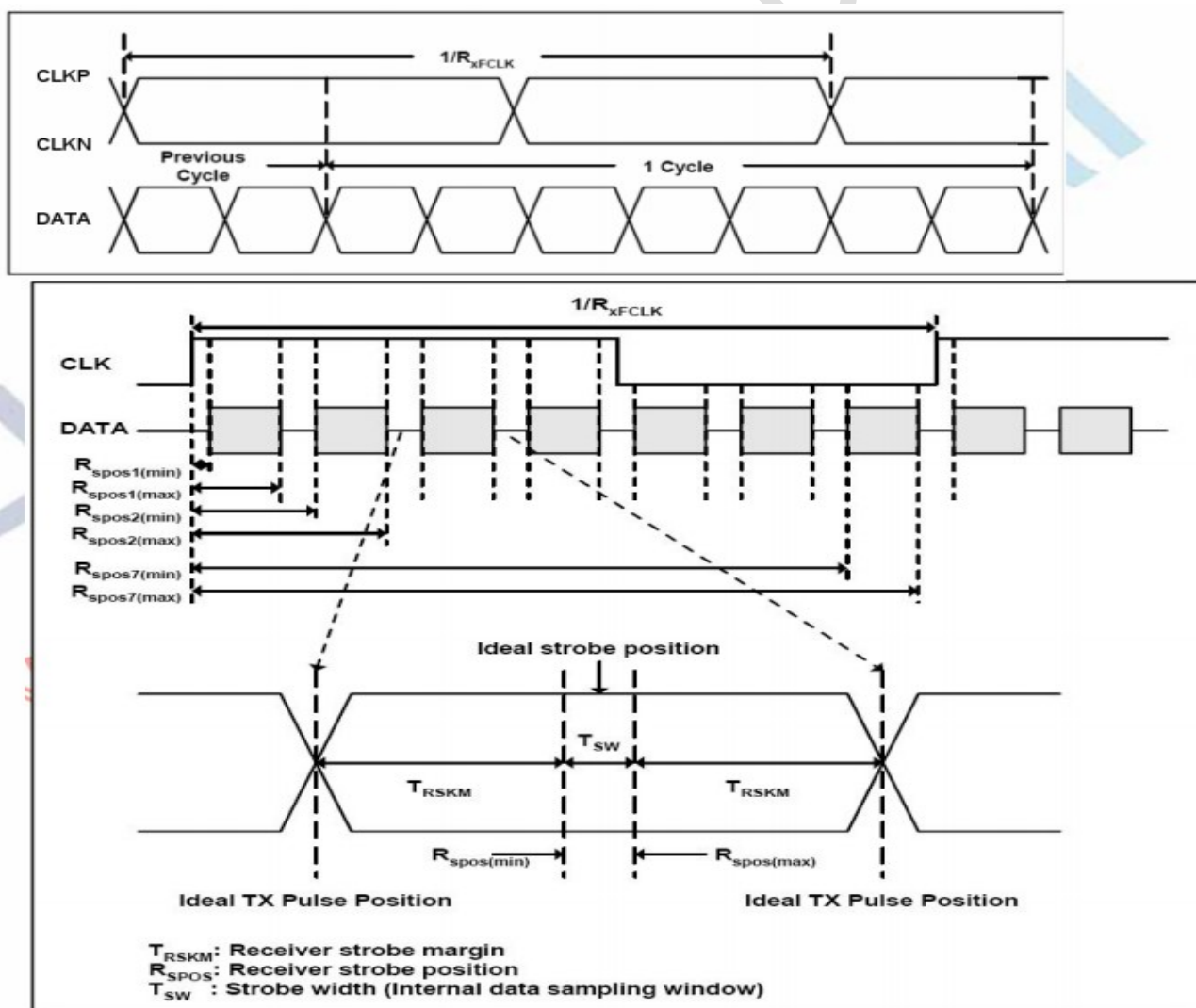
20	RXIN3-	Negative LVDS differential data input
21	RXIN3+	Positive LVDS differential data input
22	GND	Power ground
23-24	NC	No connect
25	GND	Power ground
26	NC	No connect
27	DIM0	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select
29	AVDD	Power for Analog Circuit
30	GND	Power ground
31-32	LED-	LED Cathode
33	L/R	Horizontal inversion
34	U/D	Vertical inversion
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable
37	CABCEN0	CABC H/W enable
38	VGH	Gate ON Voltage
39-40	LED+	LED Anode

4 Timing Characteristics

4.1 AC Electrical Characteristics

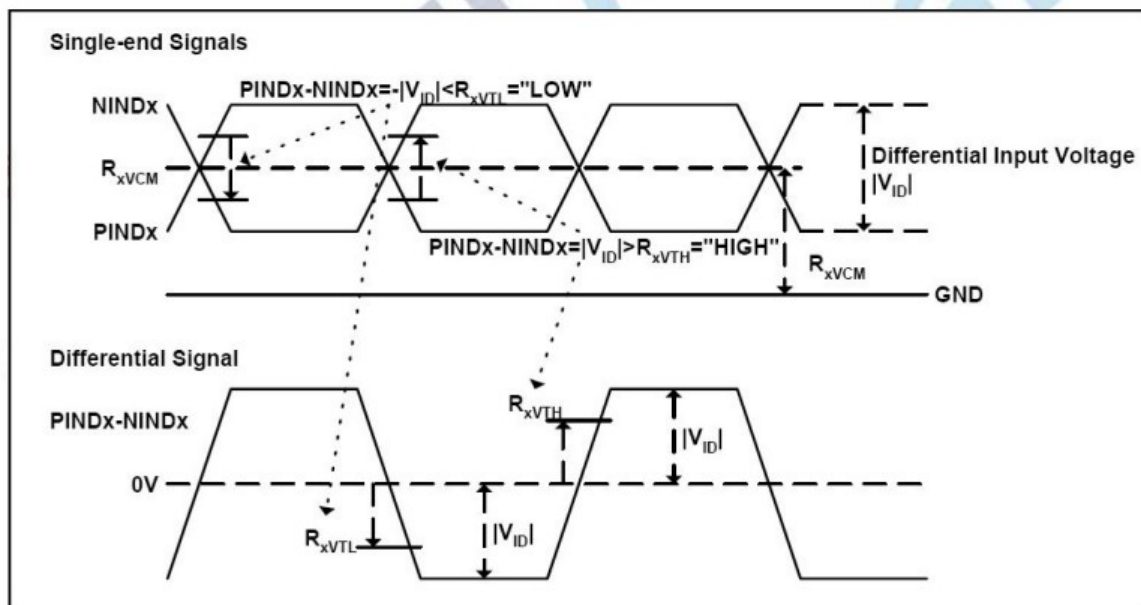
Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock frequency	R_{xFCLK}	40.8	51.2	67.2	MHz	
Input data skew margin	T_{RSKM}	500	-	-	ps	
Clock high time	T_{LVCH}	-	$4/(7 * R_{xFCLK})$	-	ns	
Clock low time	T_{LVCL}	-	$3/(7 * R_{xFCLK})$	-	ns	

4.2 Input Clock and Data Timing Diagram



4.3 DC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Differential input high Threshold voltage	R_{xVTH}	-	-	+0.1	V	$R_{xVCM}=1.2V$
Differential input low Threshold voltage	R_{xVTL}	-0.1	-	-	V	
Input voltage range (singled-end)	R_{xVIN}	0	-	2.4	V	
Differential input common mode voltage	R_{xVCM}	$ V_{ID} /2$	-	$2.4- V_{ID} /2$	V	
Differential voltage	$ V_{ID} $	0.2	-	0.6	V	
Differential input leakage current	RV_{xliZ}	-10	-	+10	μA	

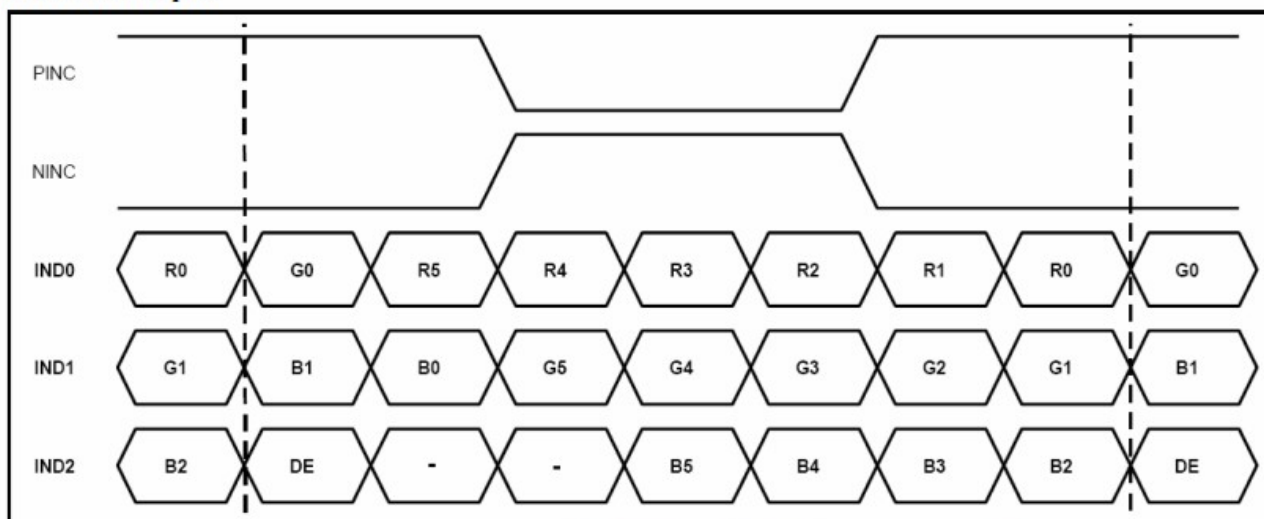


4.4 Timing

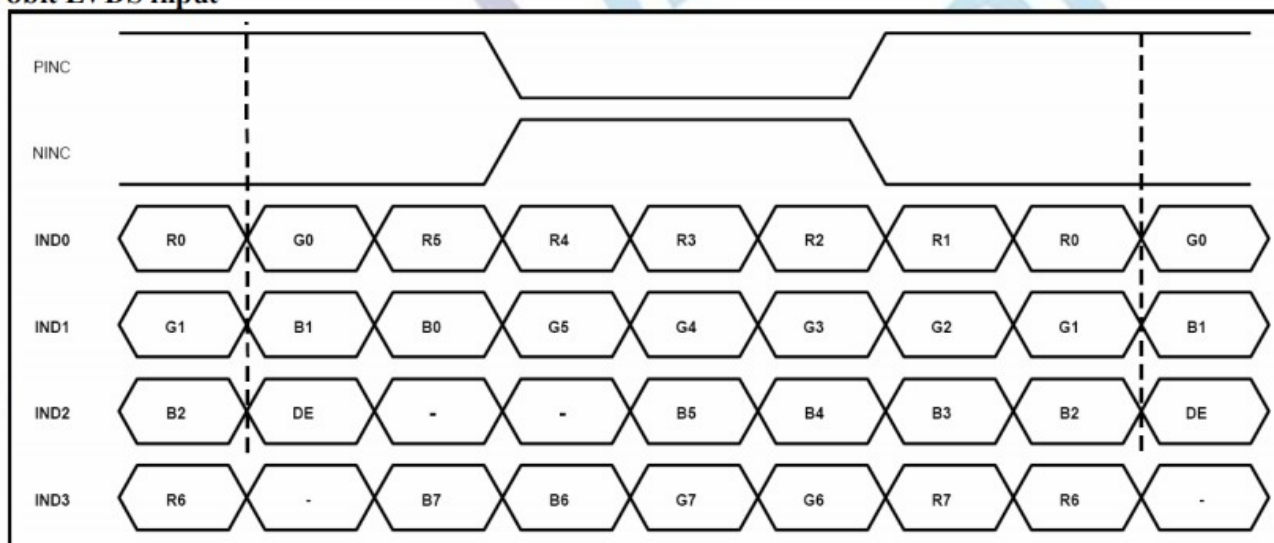
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	fclk	40.8	51.2	67.2	MHz	Frame rate =60Hz
Horizontal display area	thd	1024			DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thb	90	320	376	DCLK	
Vertical display area	tvd	600			H	
VS period time	tv	610	635	800	H	
VS Blanking	thb	10	35	200	H	

4.5 Data input format

6bit LVDS input



8bit LVDS input



Note: Support DE timing mode only, SYNC mode not supported.

5 Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
Digital Supply Voltage	VDD	-0.3	5.0	V
Analog Supply Voltage	AVDD	6.5	13.5	V
Gate On Voltage	VGH	-0.3	42	V
Gate Off Voltage	VGL	-20	0.3	V
Operating Temperature	TOP	-20	60	° C
Storage Temperature	TST	-30	70	° C
Humidity	RH	-	90%(Max 60° C)	RH

6 Electrical Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Digital Supply Voltage	VDD	3.0	3.3	3.6	V
Analog Supply Voltage	AVDD	10.8	11	11.2	V
Gate On Voltage	VGH	19.7	20	20.3	V
Gate Off Voltage	VGL	-6.5	-6.8	-7.1	V
Common Supply Voltage	VCOM	2.7	(3.7)	4.7	V

Notes:

1. VGH is TFT Gate operating voltage.
2. VGL is TFT Gate operating voltage. The low voltage level of VGL signal must be fluctuates with same phase as Vcom.
3. Vcom must be adjusted to optimize display quality, as Crosstalk and Contrast Ratio etc..
4. The value is just the reference value. The customer can optimize the setting value by the different D-IC.

7 Backlight Characteristics

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	V_f	-	9.3	-	V
Current for LED backlight	I_f	-	200	-	mA
Power consumption	Wbl	-	1860	-	mW
Uniformity	Avg	75	80	-	%
LED Life Time	-	30000	40000	-	Hrs

Note:

1. The LED life time is defined as the module brightness decrease to 50% original brightness at $T_a=25^{\circ}\text{C}$, $60\%\text{RH} \pm 5\%$.
2. The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions.
3. Typical operating life time is an estimated data.
4. Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded. Functional operation should be restricted to the conditions described under normal operating conditions.

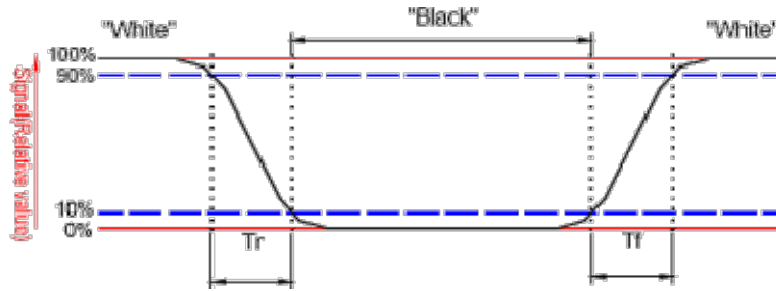
8 LCD Optical specifications

Item	Symbol	Condition	Specification			Unit	Remark
			Min	Typ	Max		
Response time (By Quick)	$Tr+Tf$	-	-	25	-	ms	Note 2
Contrast ratio	CR	-	-	800	-	-	Note 3
Surface luminance	L_v	$\theta=0^{\circ}$	-	320	-	Cd/m^2	Note 4
Luminance uniformity	Y_u	$\theta=0^{\circ}$	75	80	-	%	Note 6
NTSC	-	$\theta=0^{\circ}$	45	50	-	%	Note 6
Viewing angle	Top	$CR \geq 10$	65	75	-	Deg.	Note 7
	Bottom	$CR \geq 10$	65	75	-		
	Left	$CR \geq 10$	60	70	-		
	Right	$CR \geq 10$	65	75	-		
CIE(x,y) chromaticity	W_x	$\theta=0^{\circ}$	Typ -0.04	0.31	Typ +0.04		Note 5
	W_y			0.33			
	R_x			0.618			
	R_y			0.326			
	G_x			0.285			
	G_y			0.539			
	B_x			0.146			
	B_y			0.148			

Note 1: Ambient temperature = 25°C .

Note 2: Definition of response time:

The output signals of TRD-100 are measured when the input signals are changed to “White” (falling time) and from “White” to “Black” (rising time), respectively. The interval is between the 10% and 90% of amplitudes. Refer to figure as below.



Note 3: Definition of contrast ratio:

Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

Measured at the center area of the LCD.

Note 4: Definition of surface luminance

Surface luminance is the luminance with all pixels displaying white

Note 5: For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is based on TOPCON's BM-7 photo detector or compatible.

Size : $S \leq 4.3''$ (see Figure A B)

H,V : Active area

Light spot size=7.7mm (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure B.

measurement instrument : TOPCON's luminance meter BM-7 or compatible.

Size : $4.3 < S \leq 12.3''$ (see Figure A C)

H,V : Active area

Light spot size=7.7mm (BM-7) 50cm distance or compatible distance from the LCD surface to detector lens.

test spot position : see Figure C.

measurement instrument : TOPCON's luminance meter BM-7 or compatible.

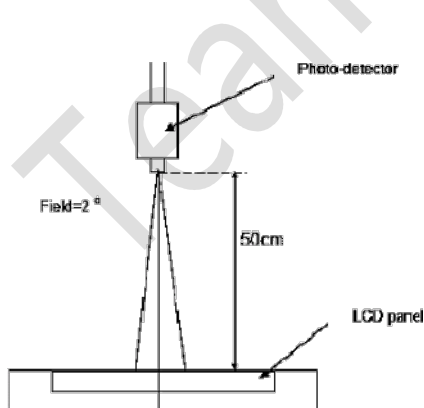


Figure A

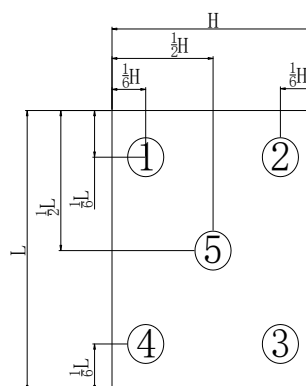


Figure B

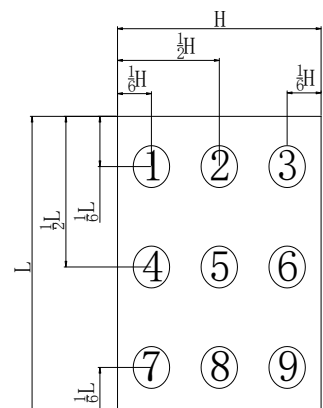


Figure C

Note 6: Definition of Luminance Uniformity

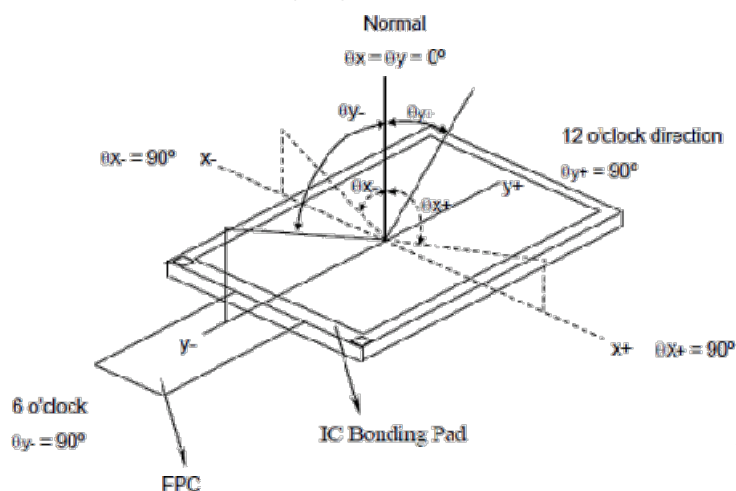
Active area is divided into 5 or 9 measuring areas, Every measuring point is placed at the center of each measuring area

Bmax: The measured maximum luminance of all measurement position.

Bmin: The measured minimum luminance of all measurement position.

Luminance Uniformity (Yu)= (Bmin/Bmax)x100%

Note 7: Definition of viewing angle



9 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	INSPECTION AFTER TEST
1	High Temperature Storage	80±2°C/96 hours	Inspection after 2~4 hours storage at room temperature and humidity. The condensation is not accepted. The sample shall be free from defects: 1. Air bubble in the LCD 2. Seal leak 3. Non-display 4. Missing segments 5. Glass crack
2	Low Temperature Storage	-30±2°C/96 hours	
3	High Temperature Operating	70±2°C/96 hours	
4	Low Temperature Operating	-20±2°C/96 hours	
5	Temperature Cycle	-30±2°C ~ 25~ 80± 2°C × 10 cycles (30 min.) (5min.) (30min.)	
6	Damp Proof Test	60°C ±5°C × 90%RH/96 hours	
7	Vibration Test	Frequency 10Hz~55Hz Stroke: 1.5mm Sweep: 10Hz~150 Hz~10Hz 2 hours For each direction of X, Y, Z	
8	Packing Drop Test	Height: 50 cm 1 corner, concrete floor	
9	Electrostatic Discharge Test	C=150pF, R=330 Ω Air: ±8KV 150pF/330Ω 30 times Contact: ±4KV, 20 times	

10 Suggestions for using LCD modules

10.1 Handling of LCM

1. The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
2. If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
3. Don't apply excessive force on the surface of the LCM.
4. If the surface is contaminated, clean it with soft cloth. If the LCM is severely contaminated, use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer. The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
5. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
6. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
7. Don't disassemble the LCM.
8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
9. Do not alter, modify or change the the shape of the tab on the metal frame.
10. Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
11. Do not damage or modify the pattern writing on the printed circuit board.
12. Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
13. Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
14. Do not drop, bend or twist LCM.

10.2 Storage

1. Store in an ambient temperature of 5 to 45 °C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
2. Storage in a clean environment, free from dust, active gas, and solvent.
3. Store in antistatic container.

